

10/049792

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10049792	FILING DATE 02/14/2002	CLASS 349	SUBCLASS 149	GAU 2871	EXAMINER Dung Thai
**APPLICANTS: Aoki Hironori;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/JP01/04824 06/07/2001					
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**FOREIGN APPLICATIONS VERIFIED: JAPAN 2000-183034 06/19/2000					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no				542-007-2	
Verified and Acknowledged Examiners's initials					
TITLE : Array substrate and display unit using it and production method for array substrate <small>U.S. DEPT. OF COM. / PAT. & TM-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
ISSUE FEE		Print Fig.	
Amount Due	Date Paid	Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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